

In the claims:

Please add claims 31-34.

31. (New): The method of claim 1, wherein the input clock signal comprises:  
a first channel of the input clock signal; and  
a second channel of the input clock signal, wherein the second channel of the input clock signal is one of a constant signal above ground potential, or a signal at the same frequency as the first channel of the input clock signal.

32. (New): The method of claim 31, wherein the output clock signal is a single-ended clock signal.

33. (New): The method of claim 2, wherein the differential clock signal comprises:  
a first channel of the input clock signal; and  
a second channel of the input clock signal, wherein the second channel of the input clock signal is one of a constant signal above ground potential, or a signal at the same frequency as the first channel of the input clock signal.

34. (New): The method of claim 9, wherein the second channel of the clock input signal is one of a constant signal at ground potential, a constant signal above ground potential, or a signal at the same frequency as the first channel of the clock input signal.